

AMENDMENTS TO THE SPECIFICATION

Please replace the paragraph on page 36, lines 15-19 with the following amended paragraph:

When it is assumed that the mutual conductance per unit gate voltage is β_{21} , the ON resistance ~~R₂₁~~ R_{on21} of the MOS-FET 21 is obtained by substituting V_{gs21} in V_{gs} of the equation (4), as represented by equation (10).

Please replace the paragraph on page 58, lines 14-26 with the following amended paragraph:

In the equation (27), R_{50} is the resistance value of the resistor 50, and β_{35} and β_{36} are the mutual conductance values per unit gate voltage of the MOS-FETs 35 and 36, respectively. In the MOS-FETs 35 and 36, a relationship of $\beta \propto (W/L)$ is established, as explained according to the equation (18) in the descriptions of the above-mentioned third embodiment. It is thus found that the output voltage V_{55} is proportional to the channel ~~width~~ widths W , provided that the channel lengths L are the same. Since the power source voltage V_{dd} is included in the numerator of the equation (27), the output power source voltage V_{55} is proportional to the power source voltage V_{dd} .